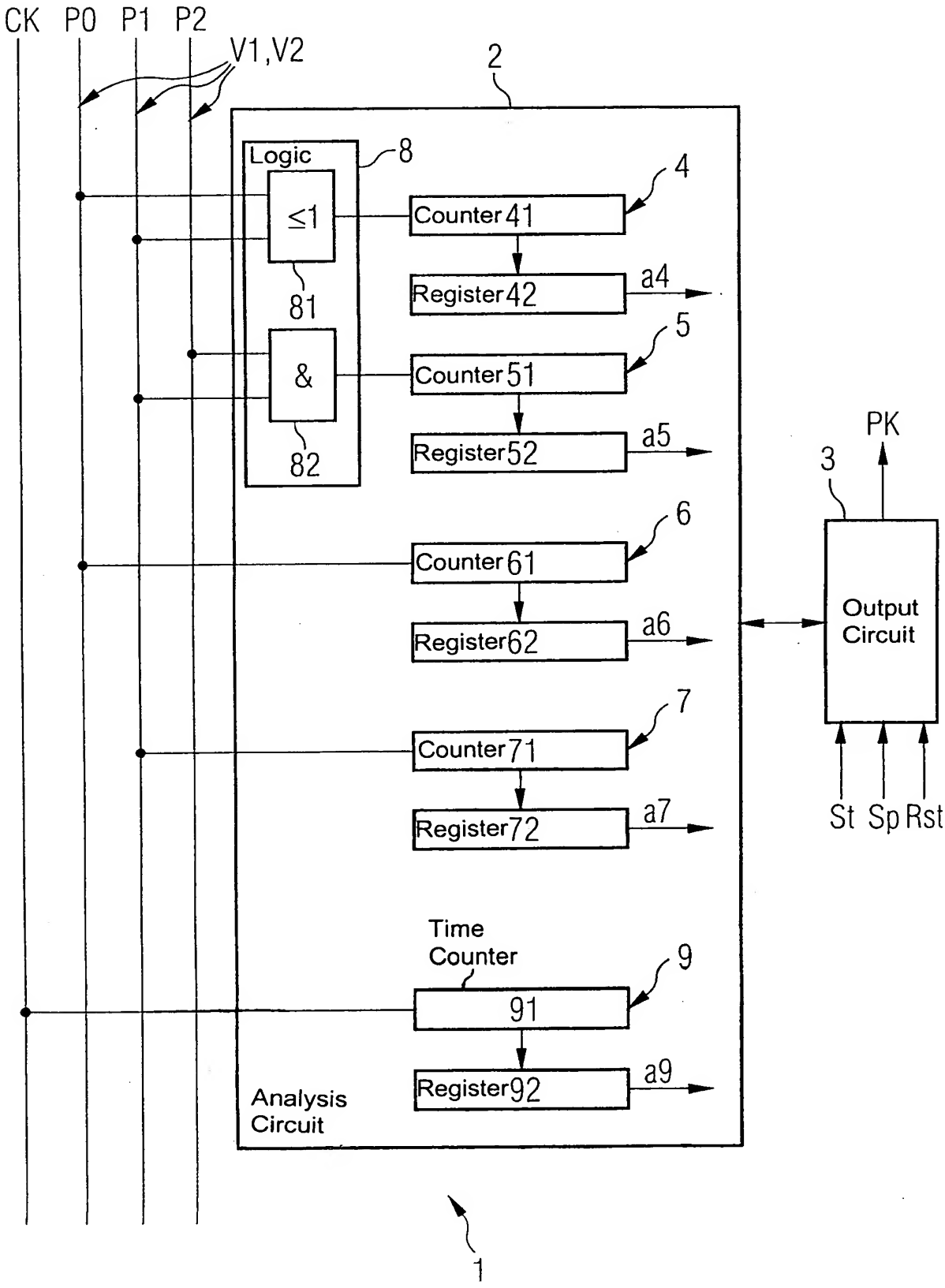
CK
P0
P1
P2
V1,V2
2
Logic
8
≤1
81
&
82
4
Counter 41
Register 42
a4
5
Counter 51
Register 52
a5
6
Counter 61
Register 62
a6
7
Counter 71
Register 72
a7
Time Counter
9
91
Register 92
a9
Analysis Circuit
PK
3
Output Circuit
St
Sp
Rst
1